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Our Case No. 7103/30

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Homayoun Talieh and David E. Weldon

Serial No. To Be Assigned

Filing Date: Herewith

For Linear Polisher and Method for Semiconductor Wafer Planarization

| Semiconductor Wafer Planarization | Semiconductor Semiconductor | Semico

## PRELIMINARY AMENDMENT

Assistant Commissioner for Patents Washington, D.C. 20231

Dear Sir:

Please enter the following amendments prior to examination:

In the Claims

Please enter new Claims 32-35 as follows: